

Amendments to the Specification:

Please amend the paragraph at page 4, line 19, to page 5, line 2, of the Specification to read as follows:

The transmitter circuitry 200 is based on a heterodyne transmitter architecture and comprises an analog base band circuit 20 in which the pre-equalized signal is pre- pared for transmission, e.g. by applying filtering, channel coding, pulse shaping or other suitable processing operations. Then, the processed base band signal is supplied to a first up-conversion stage comprising a modulator or multiplier 25 to which a signal obtained from a first oscillator 30 at a frequency of e.g. 1.5GHz is supplied in order to convert the signal frequency to the 1 .5GHz range. Then, the up-converted signal is supplied to an analog IF filter circuit 35 to suppress un- wanted frequency components generated by non-linear or other distortions. The filtered up-converted signal is then supplied to a second up-conversion stage comprising a second modulator or multiplier 40 to which an up-conversion signal at an adjustable range of 3.5 to 4.5 GHz is supplied from a controllable second oscillator 45 54. Thereby, the signal from the analog IF circuit 35 is finally up- converted to an adjustable frequency range of 3.5 to 4.5 GHz. This two-time up- converted radio frequency (RF) signal is supplied to a second filter circuit, i.e. an analog RF filter circuit 50 adapted to pass only the desired frequency range of the transmission signal supplied to the transmission antenna 55.